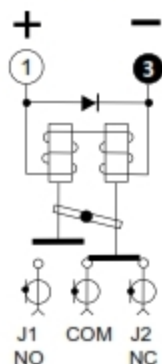
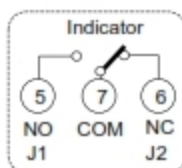
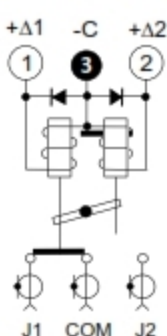


SP2T

Failsafe



Pulse Latching



TTL LOGIC TRUTH TABLE (FAILSAFE)

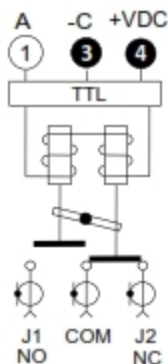
RF PATH	LOGIC INPUT "A"	INDICATOR
J1-COM	1	NO-COM
J2-COM	0	NC-COM

TTL LOGIC TRUTH TABLE (SELF CUTOFF)

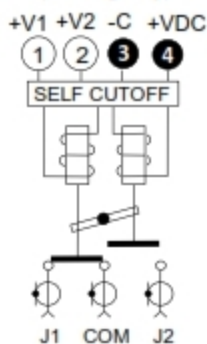
RF PATH	LOGIC INPUT A1	LOGIC INPUT A2	INDICATOR
J1-COM	1	0	J1-COM
J2-COM	0	1	J2-COM

TL logic: low "0" = 0.0V – 0.8V;
high "1" = 2.4V – 5.5V

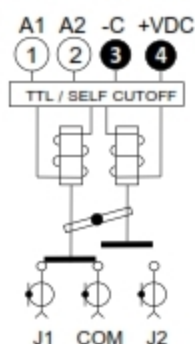
Failsafe TTL



Self Cutoff (latching only)



TTL and Self Cutoff (latching only)



NOTE: (1) TTL logic: low "0" = 0.0V – 0.8V; high "1" = 2.4V – 5.5V.
(2) "T"=50Ω termination.
(3) "NC"=Normally Closed; "NO"=Normally Open.
(4) Consult the factory for the positive COM option.