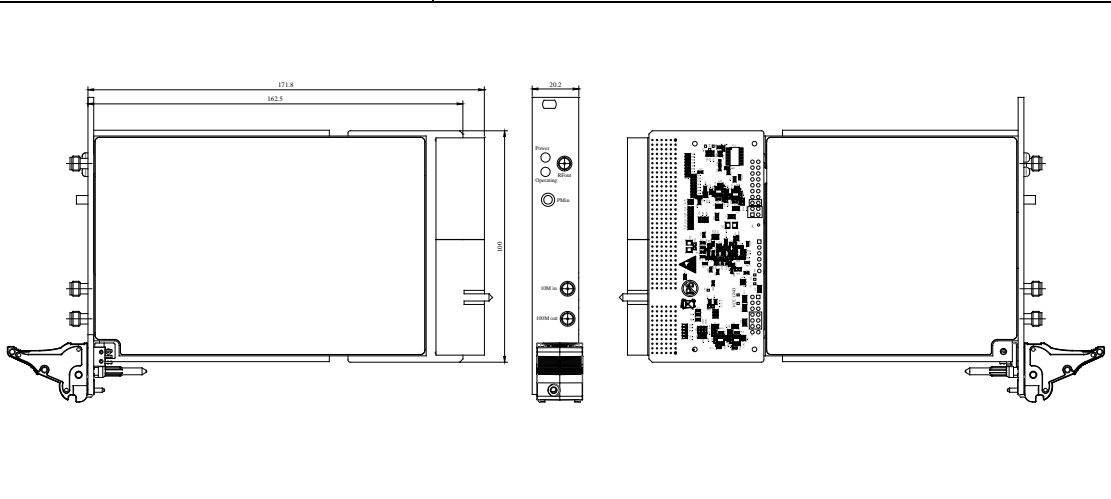


## General Signal Source PXI Module



Frequency range	Option1 1601	10MHz~6.6GHz			
	Option2 1602	10MHz~12.4GHz			
	Option3 1603	10MHz~20GHz			
	Option4 1604	10MHz~40GHz			
Step (Hz)		20			
Frequency switching (uS)		≤200			
Output level	10MHz~6.6GHz	-45dBm~+15dBm	Option5 1601P -45dBm~+37dBm		
	10MHz~12.4GHz	-45dBm~+15dBm			
	10MHz~20GHz	-25dBm~+15dBm			
	100MHz~40GHz	100MHz to <10GHz :		-25dBm~+15dBm	
10GHz to <20GHz :		-25dBm~+13dBm			
20GHz to <40GHz :		-25dBm~+13dBm			
Power step (dB)		0.1			
Output level steady (dB)		±0.5 (0~+40℃, Option1/Option2); ±1.5 (0~+40℃, Option3/Option4); ±0.2 (ALC ON)			
Power accuracy (dB)	-45dBm to <-25dBm:		±2.5 (0~+40℃)		
	-25dBm to <-15dBm:		±1.5 (0~+40℃)		
	-10dBm to <0dBm:		±1.5 (0~+40℃)		
	0dBm to <+15dBm:		±1.5 (0~+40℃) ±0.2 (ALC ON)		
Frequency temperature stability		±0.3ppm (Can be synchronized external reference)			
Frequency accuracy		±0.3ppm (Can be synchronized external reference)			
Spurious (dBc)		-70(Typical)			
Harmonics (dBc)	10MHz to <3GHz :		≤-30 (Output Level: 0dBm)		
	3GHz to <20GHz :		≤-40 (Output Level: 0dBm)		
Phase Noise	dBc/Hz@100Hz	@10GHz	≤-76	@20GHz	≤-70
	dBc/Hz@1kHz		≤-90		≤-84
	dBc/Hz@10kHz		≤-94		≤-88
	dBc/Hz@100kHz		≤-94		≤-88
	dBc/Hz@1MHz		≤-100		≤-94
Power supply		+12V			
Power consumption (W)		≤10			
<b>Pulse Modulation</b>					
Modulation depth		≥60dBc (Test Conditions: Output=+10dBm)			

Modulation pulse width	100ns~10ms
Pulse cycle	500ns~10ms
Pulse fluctuation	0.1dB
Pulse up / down edge	30nS/50nS
Pulse overshoot	--
<b>List Scan</b>	
Store the number of points	Supports up to 1024 points (External trigger)
Connector	RF connector: SMA-KFD Control and power connector: J1、J2 Not using PXI , connector: J30J-9ZKP
Dimensions	≤140×100×18mm (3U PXI one slot)
Control	SPI (Compatible PXI)
Operating temperature (°C)	0~+40
Storage temperature (°C)	-20~+70



**Notes:**

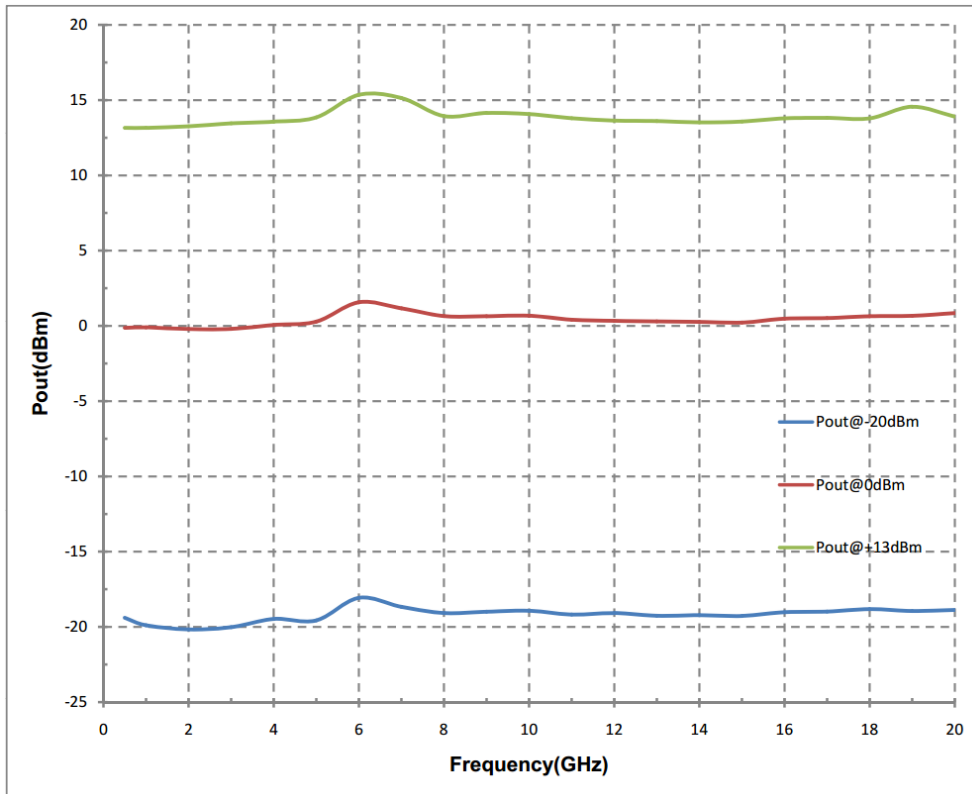
1. Internal TCXO, automatic synchronization 10MHz (Pout≥+0dBm) external reference;
2. The frequency hopping time is the hardware switching time, and does not include the external software communication time. If SPI communication is used, the rate is less than 10MHz. If serial communication is used, the software processing time is related to the baud rate and the number of bytes;
3. Option: 1601/1602/1603/1601P is one slot, Option1604 is a dual slot module.

### DB9/J30J-9 Common Interface Definition (SPI and serial control)

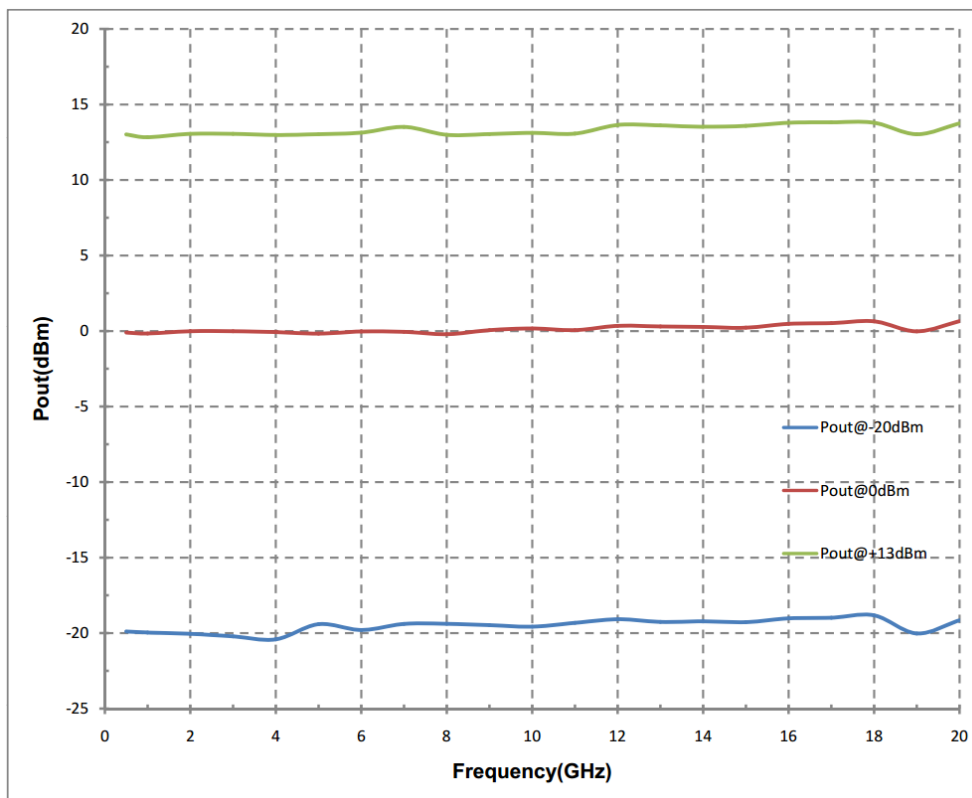
Pin number	Pin definition	Function	Pin number	Pin definition	Function
1	U/S	Communication mode selection	6	SCLK	SPI Clock
2	TXD	Serial transmission	7	MOSI	SPI DATA
3	RXD	Serial receive	8	GND	GND
4	NSS	SPI LE	9	+12V	Power
5	MISO	SPI DATA			

Notes: When the U/S is set to high, the system is serial communication, U/S is set to low, the system for the SPI communication; this pin is floating when the high.

### ALC OFF



### ALC ON



## PHASE NOISE

