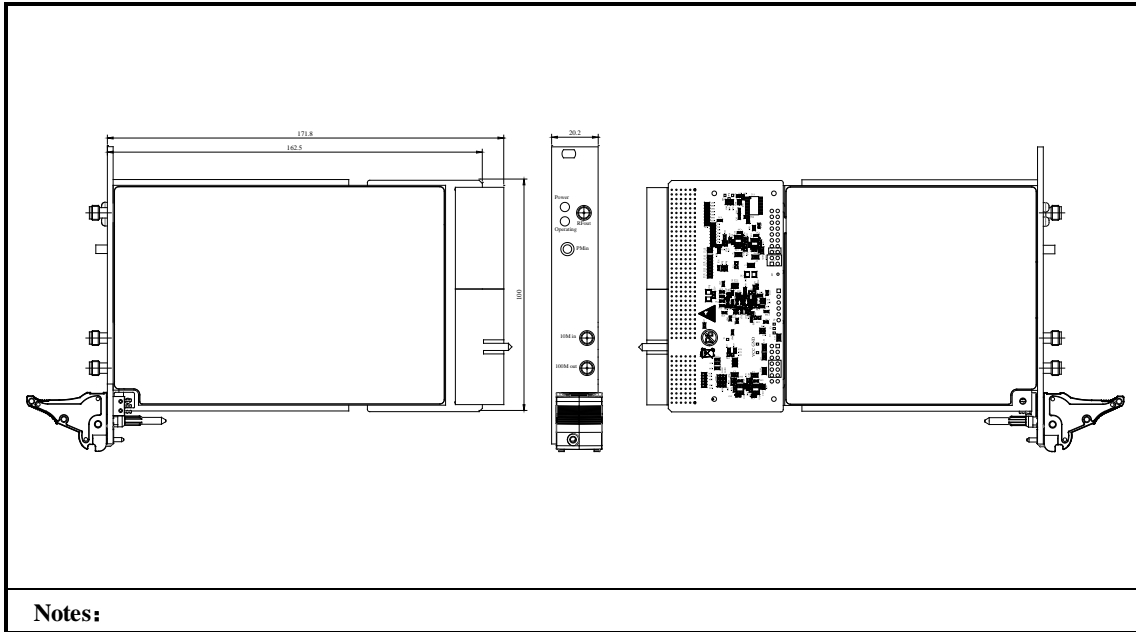


Broadband Standard Frequency Synthesizer



Description:					
1. Broadband Low Spurious Frequency Synthesizer;					
2. Using standard PXI structure and communication protocol, to facilitate system integration.					
Frequency range (MHz)		10~20000			
Step (Hz)		10			
Frequency switching (uS)		≤200			
Output level (dBm)		-20~+14			
Output level flatness (dB)		±2			
Steady frequency temperature stability		2×10^{-7} (Same as external reference)			
Frequency accuracy		2×10^{-7} (Same as external reference)			
Spurious (dBc)		≤-70			
Harmonics (dBc)		≤-35 (10~12000 MHz) ≤-20 (12000~20000MHz)			
Phase Noise	dBc/Hz@100Hz	@10GHz	≤-76	@20GHz	≤-70
	dBc/Hz@1kHz		≤-90		≤-84
	dBc/Hz@10kHz		≤-94		≤-88
	dBc/Hz@100kHz		≤-94		≤-88
	dBc/Hz@1MHz		≤-100		≤-94
Power supply (V/mA)		+12/1200 (Warm up) +12/1000 (Steady)			
Connector		RF connector: SMA-KFD Control and power connector: J1、J2 (PXI) Not using PXI , connector: J30J-9ZKP			
Dimensions		≤140×100×18mm (3U PXI one slot)			
Control		SPI/UART/ (Compatible PXI)			
Operating temperature (°C)		-40~+70			
Storage temperature (°C)		-55~+85			



Notes:

DB9/J30J-9 Common Interface Definition (SPI and serial control)

Pin number	Pin definition	Function	Pin number	Pin definition	Function
1	U/S	Communication mode selection	6	SCLK	SPI Clock
2	TXD	Serial transmission	7	MOSI	SPI DATA
3	RXD	Serial receive	8	GND	GND
4	NSS	SPI LE	9	+12V	Power
5	MISO	SPI DATA			

Notes: When the U / S is set to high, the system is serial communication, U / S is set to low, the system for the SPI communication; this pin is floating when the high.