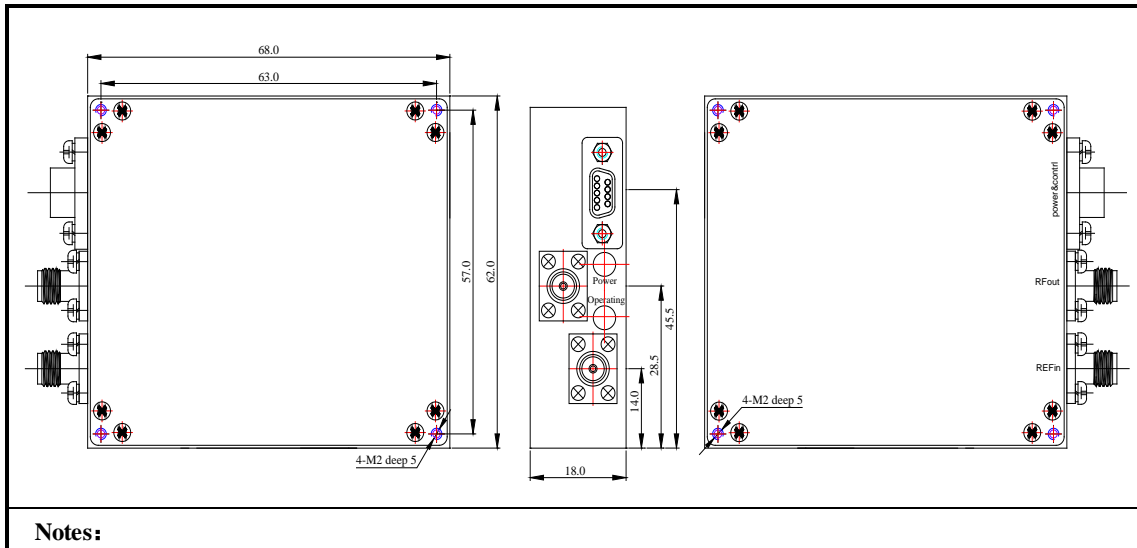


Broadband Low Spurious Frequency Synthesizer



Description:					
1. Broadband Low Spurious Frequency Synthesizer;					
2. Internal 50MHz TCXO;					
3. Switch Filter Options: Get better harmonics.					
Frequency range (MHz)		30~3000			
Step (Hz)		3			
Frequency switching (uS)		≤600			
Output Level (dBm)		≥+13			
Output Level flatness (dB)		±2			
Frequency temperature stability		±3×10 ⁻⁷ (Same as external reference)			
Frequency accuracy		±3×10 ⁻⁷ (Same as external reference)			
Spurious (dBc)		≤-70			
Harmonics (dBc)		≤-8(≤500MHz); ≤-40(>500MHz);			
Phase Noise	dBc/Hz@100Hz	@1GHz	≤-84	@2.5GHz	≤-76
	dBc/Hz@1kHz		≤-103		≤-95
	dBc/Hz@10kHz		≤-110		≤-102
	dBc/Hz@100kHz		≤-110		≤-102
	dBc/Hz@1MHz		≤-135		≤-126
Power supply (V/mA)		5V/1000			
Connector		RF connector: SMA-KFD Control and power connector: J30J-9ZKP			
Dimensions		68×62×18mm			
Control		SPI/UART			
Operating temperature (°C)		-40~+70			
Storage temperature (°C)		-55~+85			



DB9/J30J-9 Common Interface Definition (SPI and serial control)

Pin number	Pin definition	Function	Pin number	Pin definition	Function
1	U/S	Communication mode selection	6	SCLK	SPI Clock
2	TXD	Serial transmission	7	MOSI	SPI DATA
3	RXD	Serial receive	8	GND	GND
4	NSS	SPI LE	9	+12V	Power
5	MISO	SPI DATA			

Notes: When the U / S is set to high, the system is serial communication, U / S is set to low, the system for the SPI communication; this pin is floating when the high .